

Notice of References Cited	Application/Control No. 10/792,168		Applicant(s)/Patent Under Reexamination VORBACH ET AL.	
	Examiner Gopal C. Ray		Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,504,398	01-2003	Lien et al.	326/41
*	B	US-6,437,441	08-2002	Yamamoto, Hiroshi	257/758
*	C	US-6,211,697	04-2001	Lien et al.	326/41
*	D	US-6,721,830	04-2004	Vorbach et al.	710/100
*	E	US-2004/0199688	10-2004	Vorbach et al.	710/100
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	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Programmable logic machine (A programmable cell array)" by Skokan, Z.E. (abstract only) Publication Date: Oct. 1983
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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